Status of RISC-V Foundation and APAC Group

http://www.riscv.org

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RISC-V Day Shanghai

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Fudan
University

Event
Organizer

Alex Guo
Some RISC-V Event Stats...

- **RISC-V Monterey workshop Jan 14-15, 2015 in Monterey, CA**
  - Sold out: 144 (33 companies & 14 universities) Slides & videos can be found here

- **RISC-V Berkeley workshop Jun 29-30, 2015 at UC Berkeley, CA**
  - Sold out: 120 (30 companies & 20 universities) Slides & videos can be found here

- **RISC-V Redwood City workshop Jan 5-6, 2016 at Oracle Redwood City, CA**
  - Sold out: 157 (42 companies & 26 universities) Slides & videos can be found here

- **RISC-V Cambridge workshop Jul 12-13, 2016 at MIT Cambridge, MA**
  - Sold out: 252 (63 companies & 42 universities) Slides & videos can be found here

- **RISC-V Mountain View workshop Nov 29-30, 2016 at Google Mountain View, CA**
  - Sold out: 350 (107 companies & 29 universities) Slides & videos can be found here

- **RISC-V Shanghai workshop May 8-11, 2017 at NVIDIA / SJTU Shanghai, China**
  - Sold out: 287 (52 companies & 27 universities) Slides & videos can be found here

- **RISC-V Milpitas workshop Nov 28-30, 2017 at Western Digital Milpitas, CA**
  - Sold out: 498 (138 companies & 35 universities) Slides & videos can be found here

- **RISC-V Barcelona workshop May 7-9, 2018 at UPC / BSC Barcelona, Spain**
  - 3rd largest: 325 (101 companies & 25 universities) Slides & videos can be found here

- **RISC-V Day in Shanghai June 30, 2018 at Fudan University, China**
  - registration: 340
RISC-V Foundation Overview

- Incorporated August, 2015 as a 501c6 non-profit Foundation
- Membership Agreement & Bylaws ratified December 2016
- The RISC-V ISA and related standards shall remain open and license-free to all parties
  - RISC-V ISA specs and compliance test suites shall always be publicly available for download
- Only members (with commercial RISC-V products) of the Foundation in good standing can use “RISC-V” and associated trademarks, and only devices that pass the compliance tests maintained by the Foundation
Foundation Organization

- The Board of Directors consists of 7+ members, whose replacements are elected by the membership.
- The Board can amend the By-Laws of the RISC-V Foundation via a two-thirds affirmative vote.
- The Board appoints chairs of ad-hoc committees to address issues concerning RISC-V, and has the final vote of approval of the recommendation of the ad-hoc committees.
  - Technical Committee Chair – Yunsup Lee, SiFive
  - Marketing Committee Chair – Ted Marena, Microsemi
- All members of committees must be members of the RISC-V Foundation.
RISC-V Foundation Board of Directors

- Krste Asanović, Chairman
  - Professor in the EECS Department at UC Berkeley
- David Patterson, Vice-Chairman
  - Google Architect, Retired Professor Computer Science UC Berkeley
- Zvonimir Bandić
  - Senior Director of Next Generation Platform Technologies at Western Digital Corporation
- Charlie Hauck
  - CEO of Bluespec Inc.
- Rob Oshana
  - Director Global SW Development at NXP
- Frans Sijstermans
  - Vice President Engineering at NVIDIA
- Ted Speers
  - Technical Fellow, Head of Product Architecture for Microsemi’s SoC Group
 Classes of Membership

- Platinum member dues of US$25,000 per year
  - eligible for Board elections and to Chair Foundation Technical Committees, Marketing Committees and Sub-Committees
- Gold member dues of US$10,000 per year
  - eligible to Chair Foundation Technical Committees, Marketing Committees and Sub-Committees
- Silver member dues of US$5,000 per year
  - participation in all Committees and Sub-Committees
- Auditor member dues of US$2,500 per year (non-voting)
  - participation in all Committees and Sub-Committees
- Individual member dues of US$99 per year (non-voting)
  - participation in all Committees and Sub-Committees
RISC-V Foundation Growth History
August 2015 (7) to April 2018 (150+)

RISC-V Day Shanghai
RISC-V Members in 25 Countries Around the World!!
Representing 55% of the global population!!
RISC-V APAC Regional Task Group

- Group Chairs:
  - Alex Guo, individual
  - Naomi Tsujioka, Software Hardware Consulting Group
- Members from 17 companies/organizations + individuals
- RISC-V Day in Shanghai (Today)
- RISC-V Workshop in IIT Madras Chennai (July 18-19)
  - Registration is open
- RISC-V Day in Tokyo (Oct. 18)
- RISC-V Workshop in NTHU Taiwan (March 2019)
Existing Books for RISC-V
New Books for RISC-V

By Bob Hu 胡振波
RISC-V ISA & Foundation Summary

- The free and open RISC-V ISA is enabling a new innovation frontier for all computing devices
- Strong Industry Support: 150+ commercial/academic members
- RISC-V Twitter http://twitter.com/risc_v @risc_v
- RISC-V LinkedIn Page
  - http://www.linkedin.com/company/risc-v-foundation
- RISC-V Wechat groups: RISC-V 讨论群[*]

- RISC-V Chennai workshop registration is open
  - July 18th - 19th 2018, IIT Madras Chennai, India
Thank You !!