

One Rocket Architecture-Based SoC Chip on 55nm Technologies and Its Future Evolution

Introduce

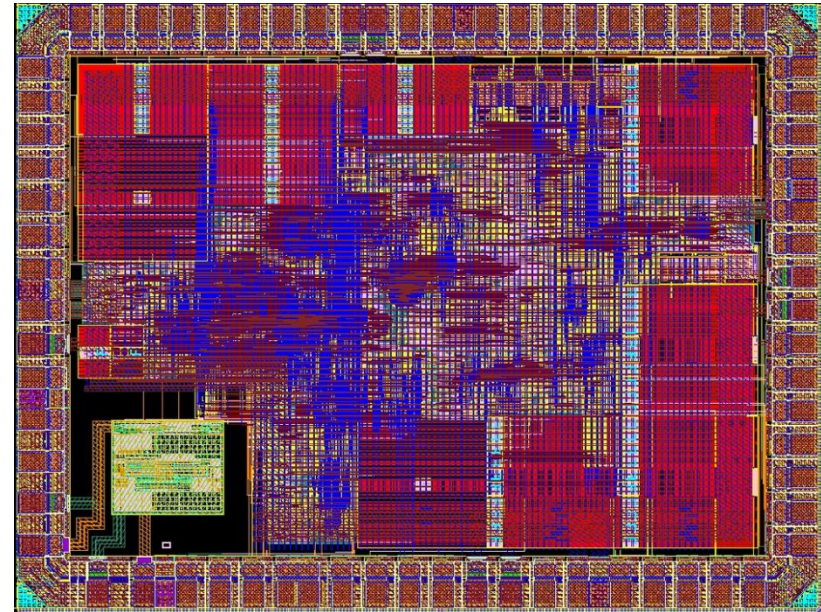
Feature

- 16 Pin GPIO
- I2C,CAN,SPI
- 2 UART , 2 PWM
- 16MB QSPI Flash
- RTOS suport

Why use rocketchip

- 1.Many well-defined subcomponents
- 2.A good extensibility for Architecture Evolution
- 3.Reduce project cycle

It took only one and a half months from project launch to tape out.



SMIC 55nm Low Leakage 1.2v/2.5v
rv32imcf with 64KICache 64KDTIM
Max Freqeunce@96MHz
Tape Out@12/04/2018

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RoadMap

